

SEMITRANS[®] 3

Trench IGBT Modules

SKM200GBD126D

Features

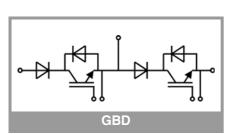
- Trench = Trenchgate technology
- V_{CE(sat)} with positive temperature
- coefficient
 High short circuit capability, self limiting to 6 x l_C
- UL recognized, file no. E63532

Typical Applications*

• Current source inverter

Remarks

- The Fig.1 to Fig.9 are based on measurements of the SKM200GB126D
- The series diodes (FWD) have the data of the inverse diodes of the SKM300GB126D



Absolute	Maximum Rating	6		
Symbol	Conditions		Values	Unit
IGBT				
V _{CES}	T _j = 25 °C		1200	V
lc	T 150.00	T _c = 25 °C	264	А
	− T _j = 150 °C	T _c = 80 °C	186	А
I _{Cnom}			150	А
I _{CRM}	$I_{CRM} = 2 x I_{Cnom}$		300	А
V _{GES}			-20 20	V
t _{psc}	$V_{CC} = 900 V$ $V_{GE} \le 15 V$ $V_{CES} \le 1200 V$	T _j = 125 °C	10	μs
Tj			-40 150	°C
Inverse d	iode			
V _{RRM}	T _j = 25 °C		1200	V
I _F	T _j = 150 °C	T _c = 25 °C	34	А
		T _c = 80 °C	23	А
I _{Fnom}			30	А
I _{FRM}	$I_{FRM} = 2 x I_{Fnom}$		60	А
I _{FSM}	t _p = 10 ms, sin 180°, T _j = 25 °C		414	А
Tj			-40 150	°C
Freewhee	eling diode			
V _{RRM}	T _j = 25 °C		1200	V
IF	T _i = 150 °C	T _c = 25 °C	250	А
	$T_j = 150$ C	T _c = 80 °C	169	А
I _{Fnom}		-	200	А
I _{FRM}	$I_{FRM} = 2 x I_{Fnom}$		400	А
I _{FSM}	$t_p = 10 \text{ ms}, \sin 180^\circ, T_j = 25 ^\circ\text{C}$		1656	Α
Tj			-40 150	°C
Module				
I _{t(RMS)}			500	А
T _{stg}	module without TIN	Λ	-40 125	°C
Visol	AC sinus 50 Hz, t =	1 min	4000	V

Characte	eristics					
Symbol	Conditions		min.	typ.	max.	Unit
IGBT						
V _{CE(sat)}	I _C = 150 A	T _j = 25 °C		1.71	2.10	V
	V _{GE} = 15 V chiplevel	T _j = 125 °C		2.00	2.45	V
V _{CE0} chip	chiplevel	T _j = 25 °C		1.00	1.20	V
	chipievei	T _j = 125 °C		0.90	1.10	V
r _{CE}	V _{GE} = 15 V	T _j = 25 °C		4.7	6.0	mΩ
	chiplevel	T _j = 125 °C		7.3	9.0	mΩ
V _{GE(th)}	$V_{GE}=V_{CE}, I_C=6 \text{ mA}$		5	5.8	6.5	V
I _{CES}	$V_{GE} = 0 \text{ V}, V_{CE} = 1200 \text{ V}, T_j = 25 ^{\circ}\text{C}$				2.0	mA
Cies	V _{CE} = 25 V V _{GE} = 0 V	f = 1 MHz		10.7		nF
C _{oes}		f = 1 MHz		0.56		nF
C _{res}		f = 1 MHz		0.48		nF
Q_{G}	V _{GE} = - 8 V+ 20 V			1530		nC
R _{Gint}	$T_j = 25 \ ^{\circ}C$			5.0		Ω



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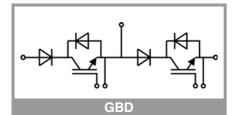
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Characte	eristics					
Symbol	Conditions		min.	typ.	max.	Unit
IGBT						
t _{d(on)}	V _{CC} = 600 V	T _i = 125 °C		260		ns
t _r	I _C = 150 A Voc = +15/-15 V	T _i = 125 °C		40		ns
Eon	V _{GE} = +15/-15 V R _{G on} = 1.5 Ω	T _i = 125 °C		18		mJ
t _{d(off)}	$R_{G \text{ off}} = 1.5 \Omega$	T _i = 125 °C		540		ns
t _f		T _j = 125 °C		110		ns
E _{off}		T _j = 125 °C		24		mJ
R _{th(j-c)}	per IGBT	<u> </u>			0.13	K/W
R _{th(c-s)}	per IGBT (λ_{grease} =0.81 W/(m*K))			0.036		K/W
R _{th(c-s)}	per IGBT, pre-applied phase change material			0.033		K/W
Inverse d	iode					
$V_F = V_{EC}$	I _F = 30 A	T _j = 25 °C		2.00	2.50	V
	V _{GE} = 0 V chiplevel	T _j = 125 °C		1.80	2.30	v
V _{F0}		T _i = 25 °C		1.10	1.45	V
	- chiplevel	T _i = 125 °C		0.85	1.20	V
r _F	chiplevel	T _i = 25 °C		30	35	mΩ
		T _j = 125 °C		32	37	mΩ
I _{RRM}	$I_F = 15 \text{ A}$ di/dt _{off} = 150 A/µs V _{GE} = ±15 V	T _j = 125 °C		12		Α
Q _{rr}		T _j = 125 °C		1		μC
E _{rr}	$V_{GE} = \pm 15 V$ $V_{CC} = 600 V$	T _j = 125 °C		-		mJ
R _{th(j-c)}	per diode				1.5	K/W
R _{th(c-s)}	per diode (λ_{grease} =0.81 W/(m*K))			0.078		K/W
R _{th(c-s)}	per diode, pre-applied phase change material			0.076		K/W
Freewhee	eling diode					
$V_F = V_{EC}$	$I_{\rm F} = 200 {\rm A}$	T _j = 25 °C		1.60	1.80	V
	V _{GE} = 0 V chiplevel	T _j = 125 °C		1.60	1.80	V
V _{F0}		T _j = 25 °C		1.00	1.10	V
	- chiplevel	T _j = 125 °C		0.80	0.90	V
r _F	ahialayal	T _j = 25 °C		3.0	3.5	mΩ
	- chiplevel	T _j = 125 °C		4.0	4.5	mΩ
I _{RRM}	I _F = 200 A	T _j = 125 °C		290		Α
Q _{rr}	di/dt _{off} = 6200 A/μs V _{GE} = ±15 V	T _j = 125 °C		44		μC
Err	$V_{GE} = \pm 15 V$ $V_{CC} = 600 V$	T _j = 125 °C		18		mJ
R _{th(j-c)}	per diode	1			0.25	K/W
R _{th(c-s)}	per diode ($\lambda_{\text{grease}}=0$	0.81 W/(m*K))	L	0.043		K/W
R _{th(c-s)}	per diode, pre-appl material			0.041		K/W





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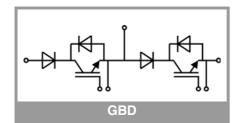
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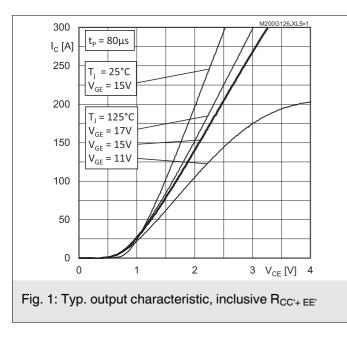
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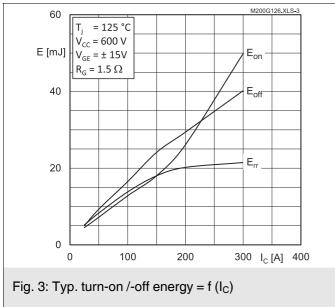
Remarks

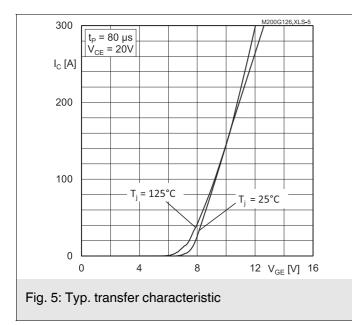
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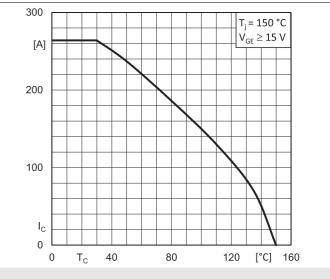
Characte	ristics					
Symbol	Conditions		min.	typ.	max.	Unit
Module						
L _{CE}				15		nH
R _{CC'+EE'}	measured per switch	T _C = 25 °C		0.35		mΩ
		T _C = 125 °C		0.5		mΩ
Rth _{(c-s)1}	per module			0.01		K/W
Rth _{(c-s)2}	including thermal coupling, Ts underneath module $(\lambda_{\text{grease}}=0.81 \text{ W/(m*K)})$		0.015			K/W
Rth _{(c-s)2}	including thermal coupling, Ts underneath module, pre-applied phase change material			0.014		K/W
Ms	to heat sink M6		3		5	Nm
Mt		to terminals M6	2.5		5	Nm
	1		1			Nm
w		I			325	g

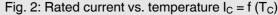


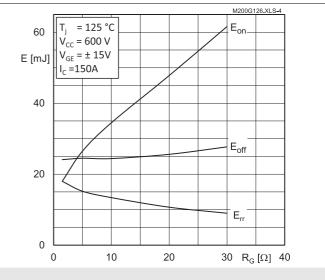


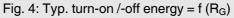


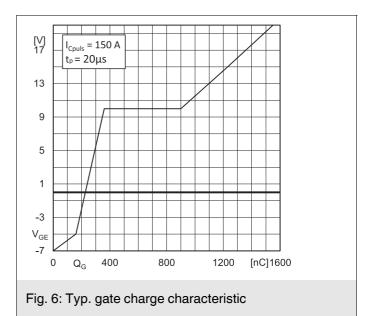


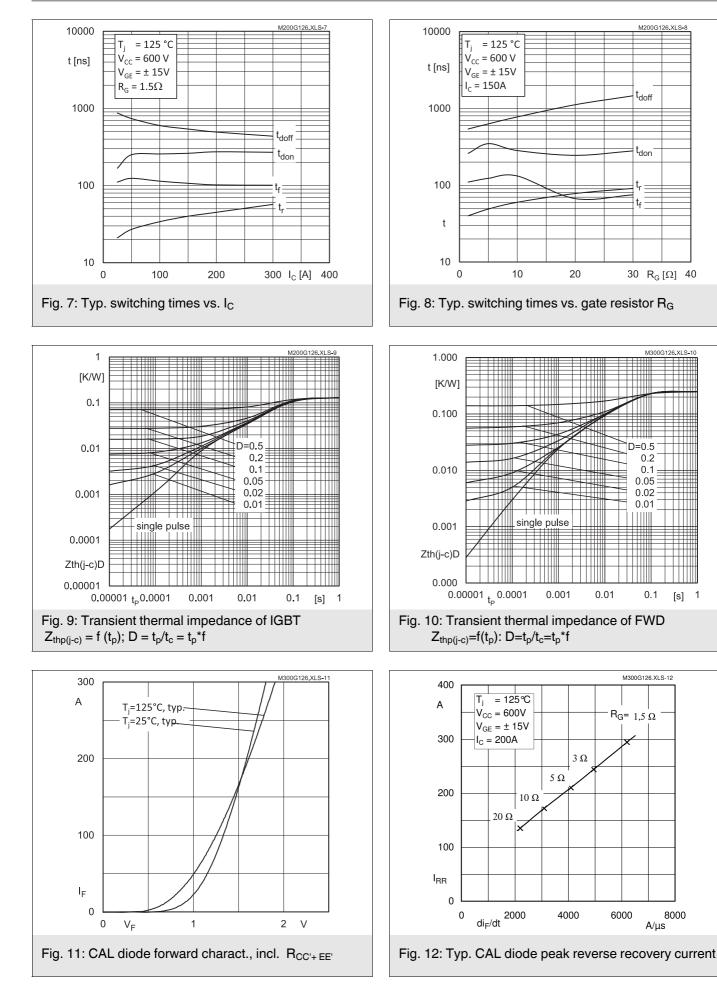












8000

A/µs

M200G126.XLS-

t_f -

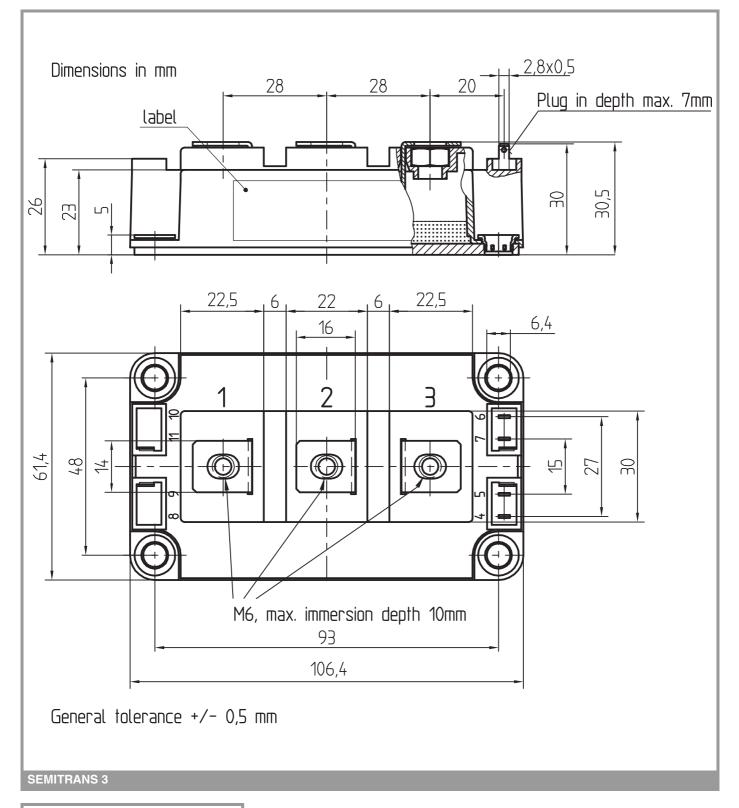
M300G126.XLS-

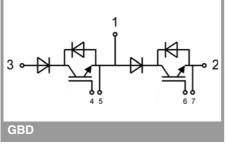
0.2

01

0.1

[s] 1





This is an electrostatic discharge sensitive device (ESDS), international standard IEC 60747-1, chapter IX.

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